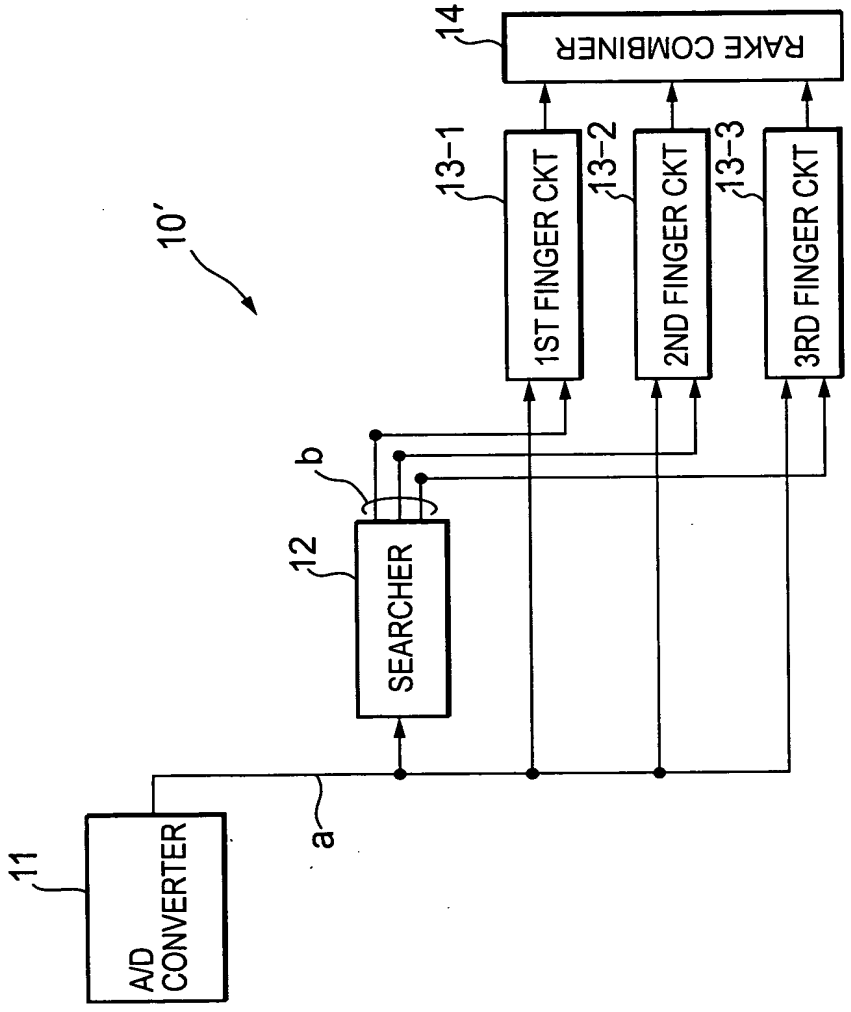


FIG.1 PRIOR ART

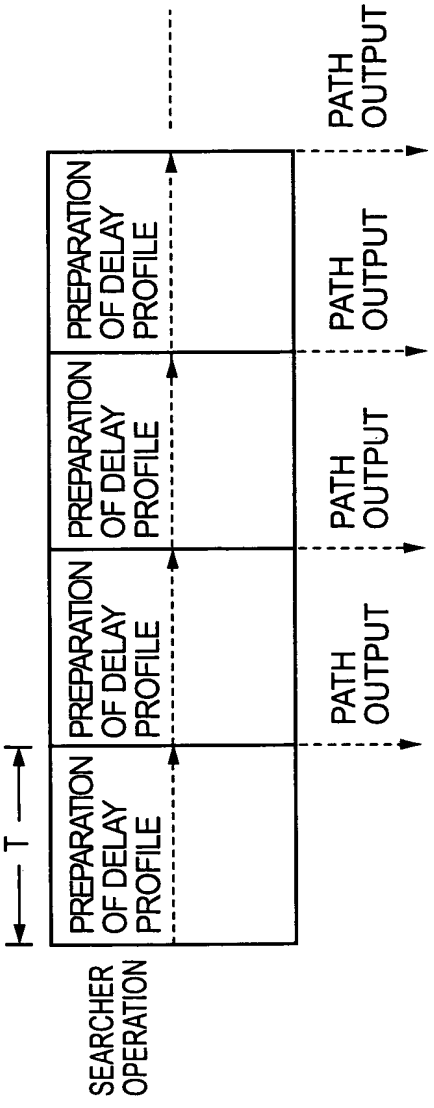


FIG.2 PRIOR ART

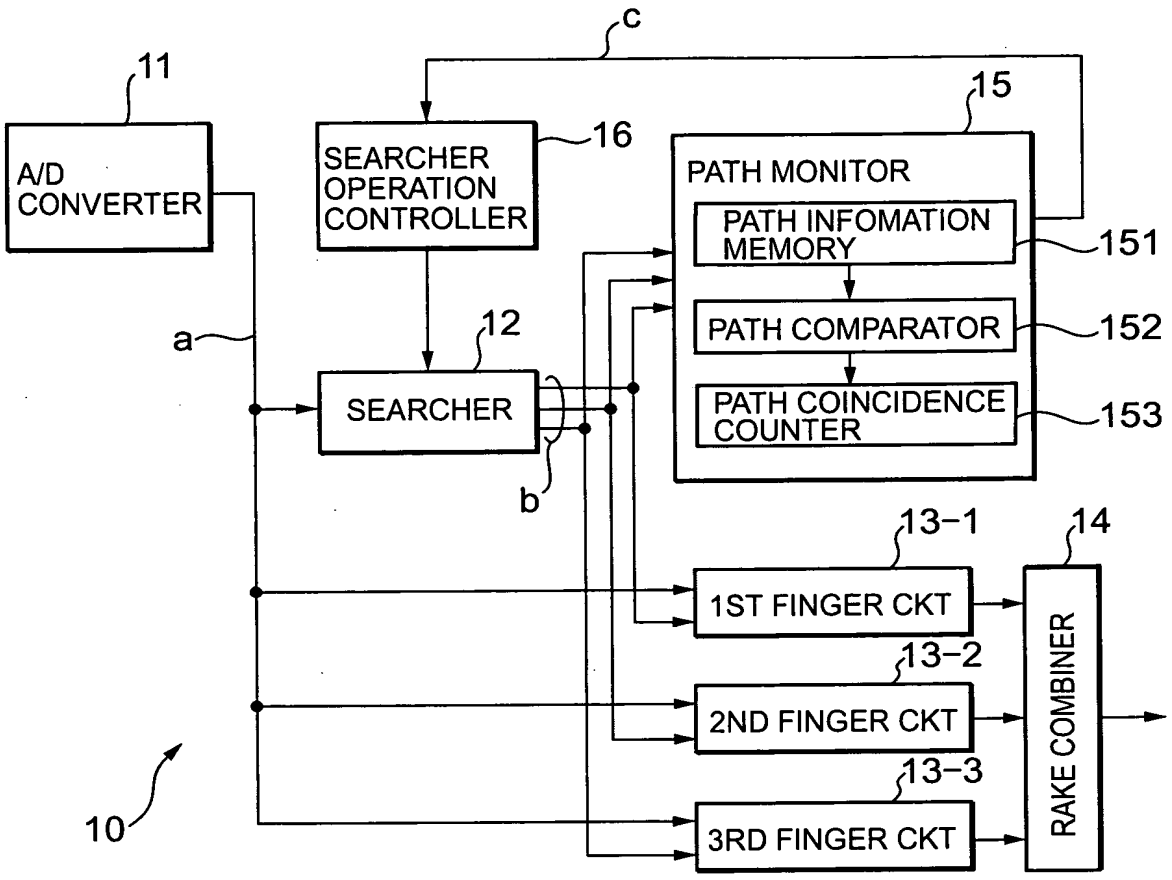

c
11
A/D CONVERTER
SEARCHER OPERATION CONTROLLER
16
15
PATH MONITOR
PATH INFOMATION MEMORY
151
PATH COMPARATOR
152
PATH COINCIDENCE COUNTER
153
a
12
SEARCHER
b
13-1
14
1ST FINGER CKT
13-2
2ND FINGER CKT
13-3
3RD FINGER CKT
RAKE COMBINER
10

FIG.3

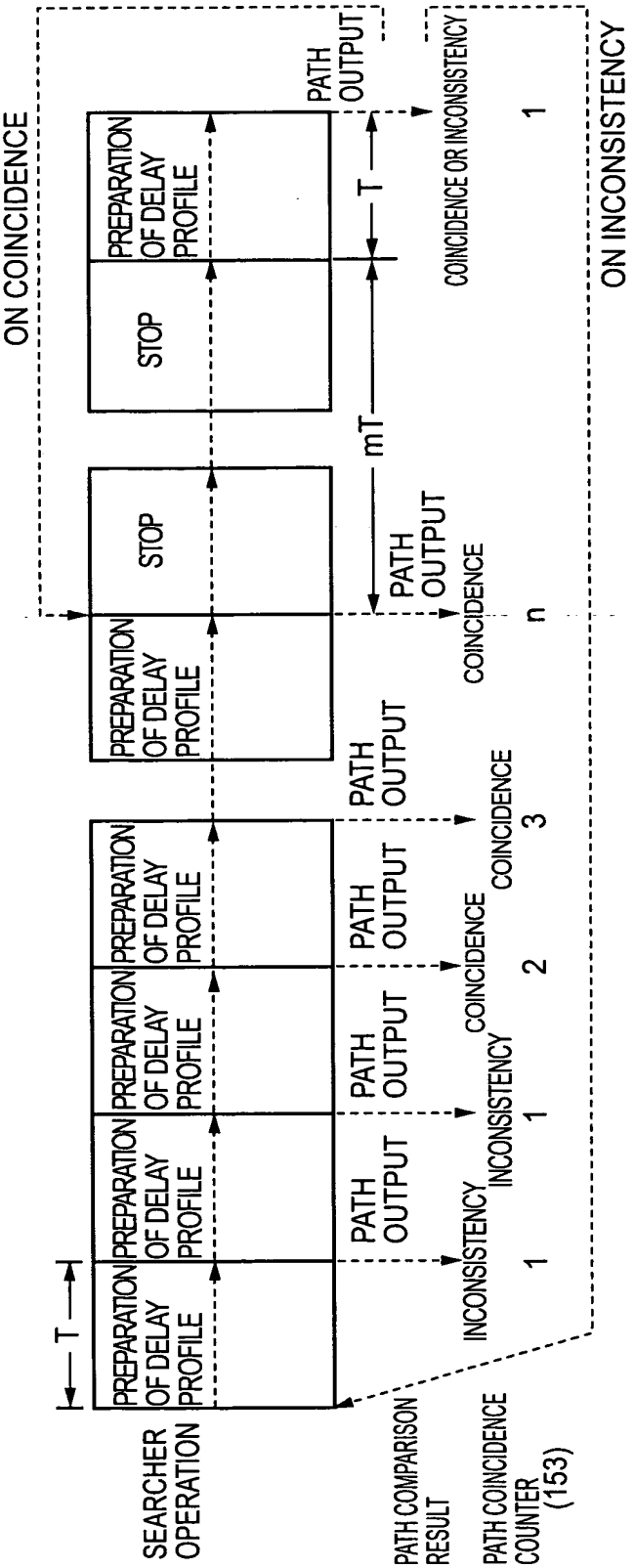

ON COINCIDENCE
ON INCONSISTENCY
SEARCHER OPERATION
PATH COMPARISON RESULT
PATH COINCIDENCE COUNTER (153)
T
PREPARATION OF DELAY PROFILE
STOP
PATH OUTPUT
INCONSISTENCY
COINCIDENCE
COINCIDENCE OR INCONSISTENCY
mT
1
2
3
n

FIG.4

FIG.5

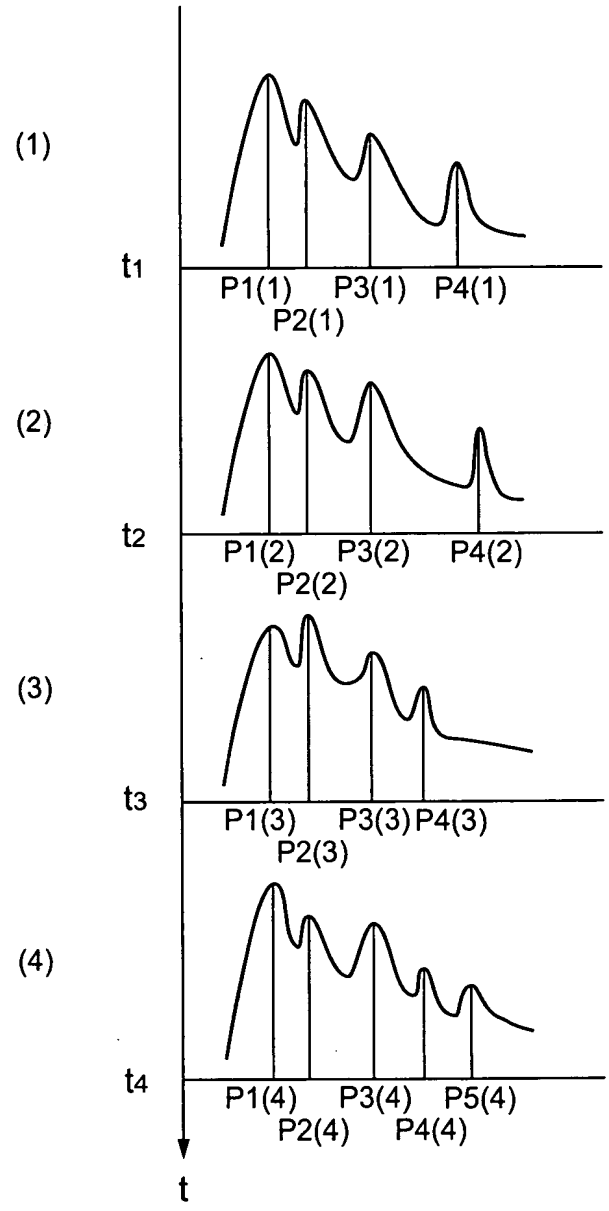
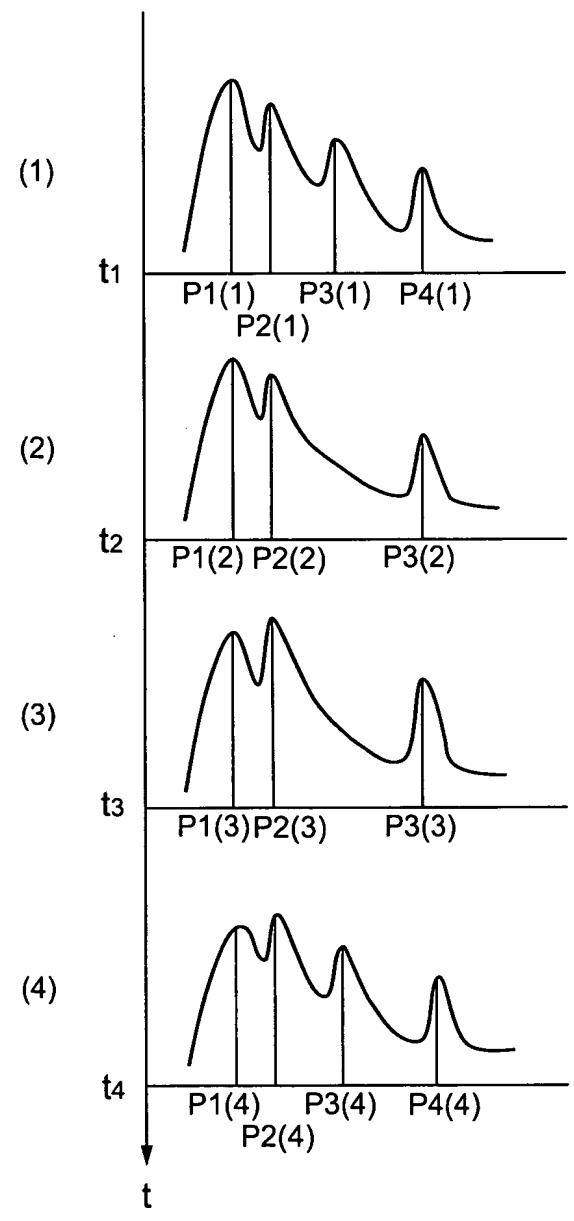

(1)
t1
P1(1)
P3(1)
P4(1)
P2(1)
(2)
t2
P1(2)
P3(2)
P4(2)
P2(2)
(3)
t3
P1(3)
P3(3)
P4(3)
P2(3)
(4)
t4
P1(4)
P3(4)
P5(4)
P2(4)
P4(4)
t

FIG.6


(1)
t1
P1(1)
P2(1)
P3(1)
P4(1)
(2)
t2
P1(2)
P2(2)
P3(2)
(3)
t3
P1(3)
P2(3)
P3(3)
(4)
t4
P1(4)
P2(4)
P3(4)
P4(4)
t

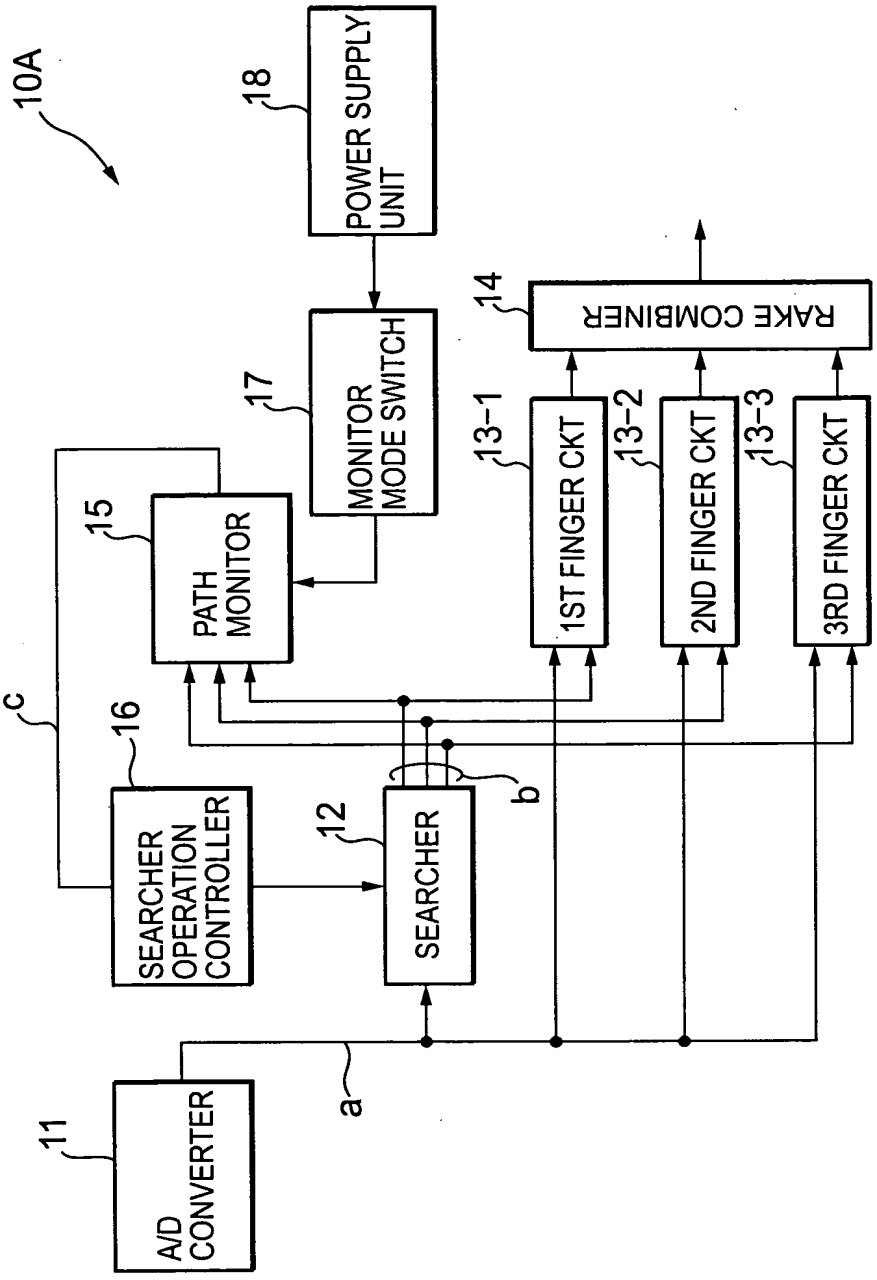

10A
18
POWER SUPPLY UNIT
17
MONITOR MODE SWITCH
14
RAKE COMBINER
13-1
1ST FINGER CKT
13-2
2ND FINGER CKT
13-3
3RD FINGER CKT
15
PATH MONITOR
c
16
SEARCHER OPERATION CONTROLLER
12
SEARCHER
b
11
A/D CONVERTER
a

FIG.7